



Applicant : Eidson et al.

Art Unit : 2183

Serial No. : 09/994,483

Examiner : Kevin P. Rizzuto

Filed : November 26, 2001

Title : SYSTEM-ON-CHIP BREAKPOINT SYNCHRONIZATION

MAIL STOP AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

NOTICE OF APPEAL

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the action dated August 4, 2005, finally rejecting claims 37.

A petition for an extension of time under 37 CFR §1.136 to extend the time to respond to the final rejection for 2 month(s) to and including January 4, 2006 is enclosed.

A check in the amount of \$500 for the appeal fee is enclosed. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: January 4, 2006

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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January 4, 2006

Date of Deposit

Signature

Julie H. Giordano

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